

099020 65220660

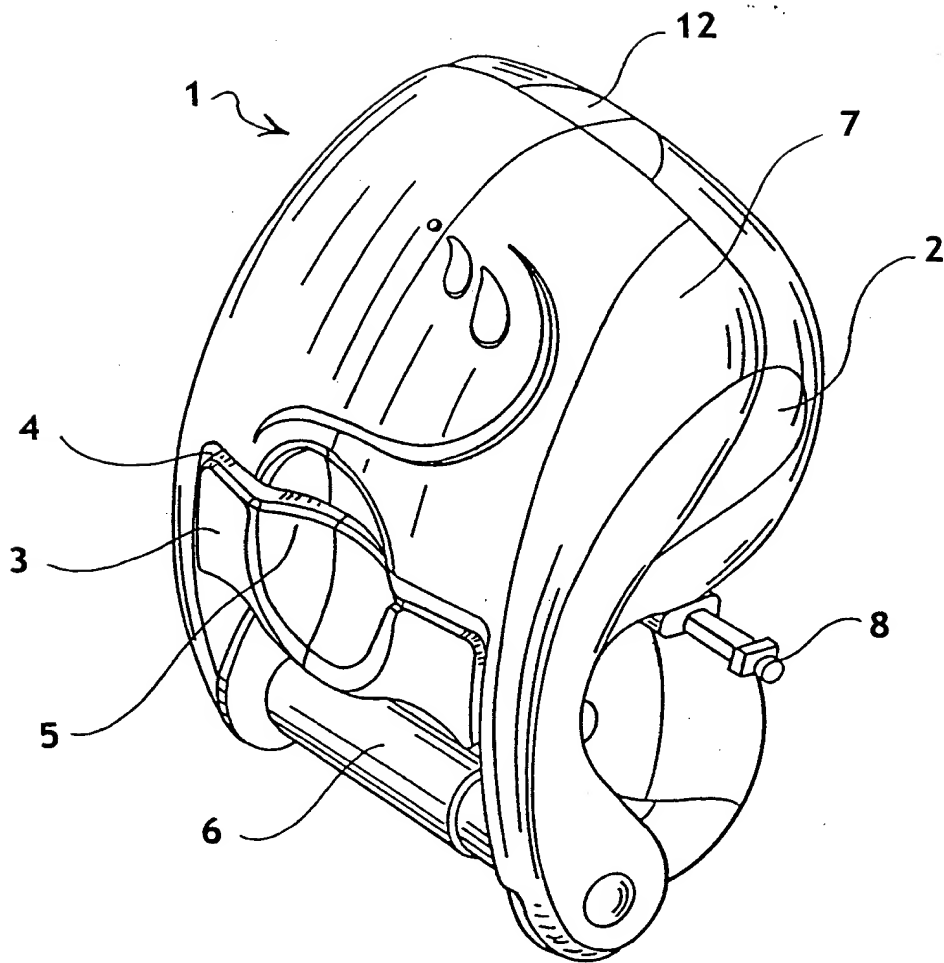


FIG. 1

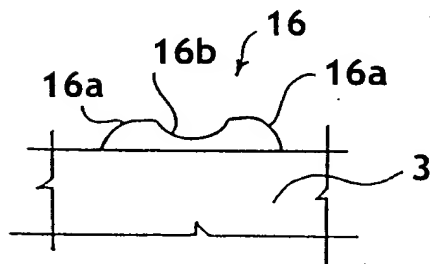


FIG. 2A

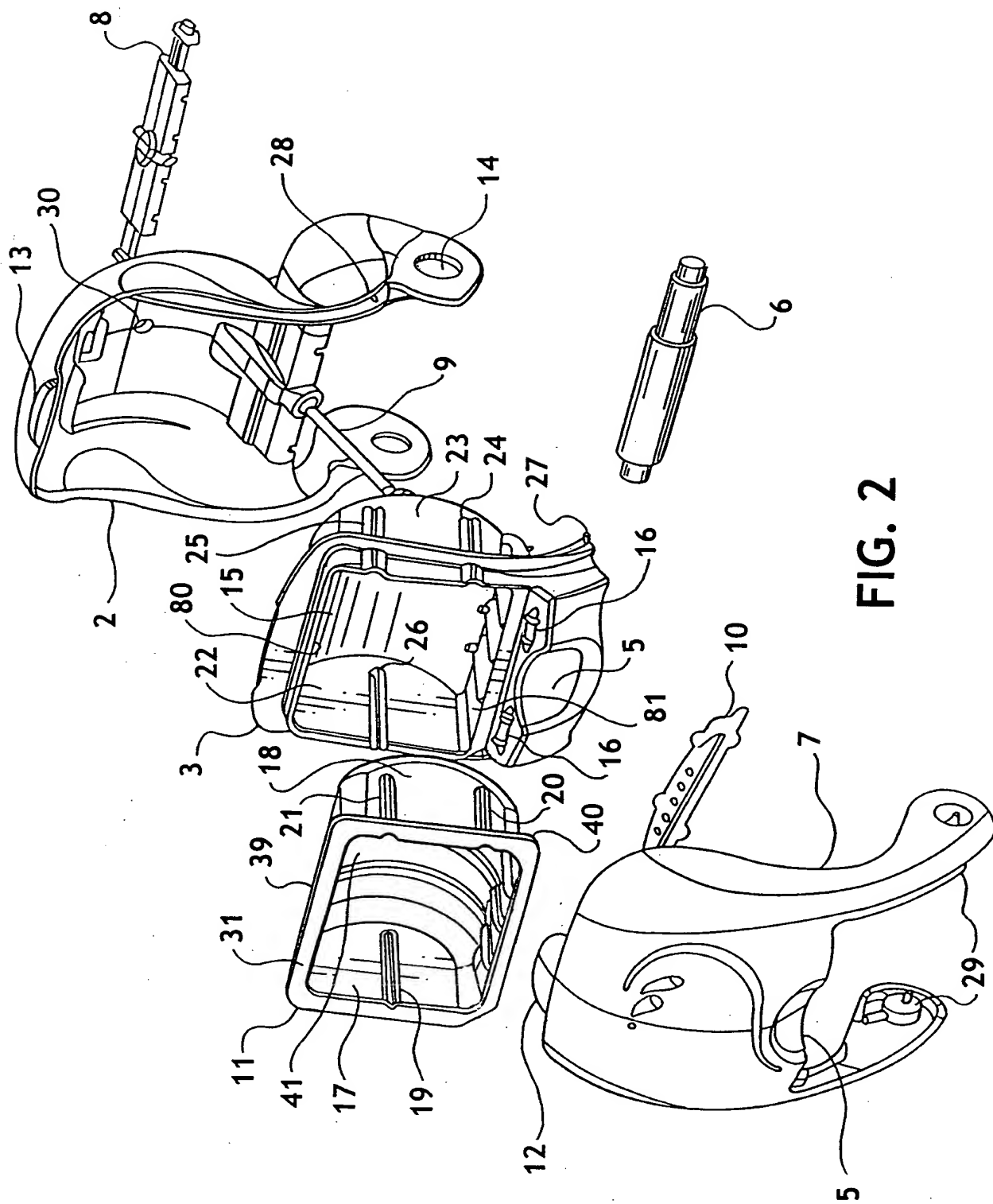


FIG. 2

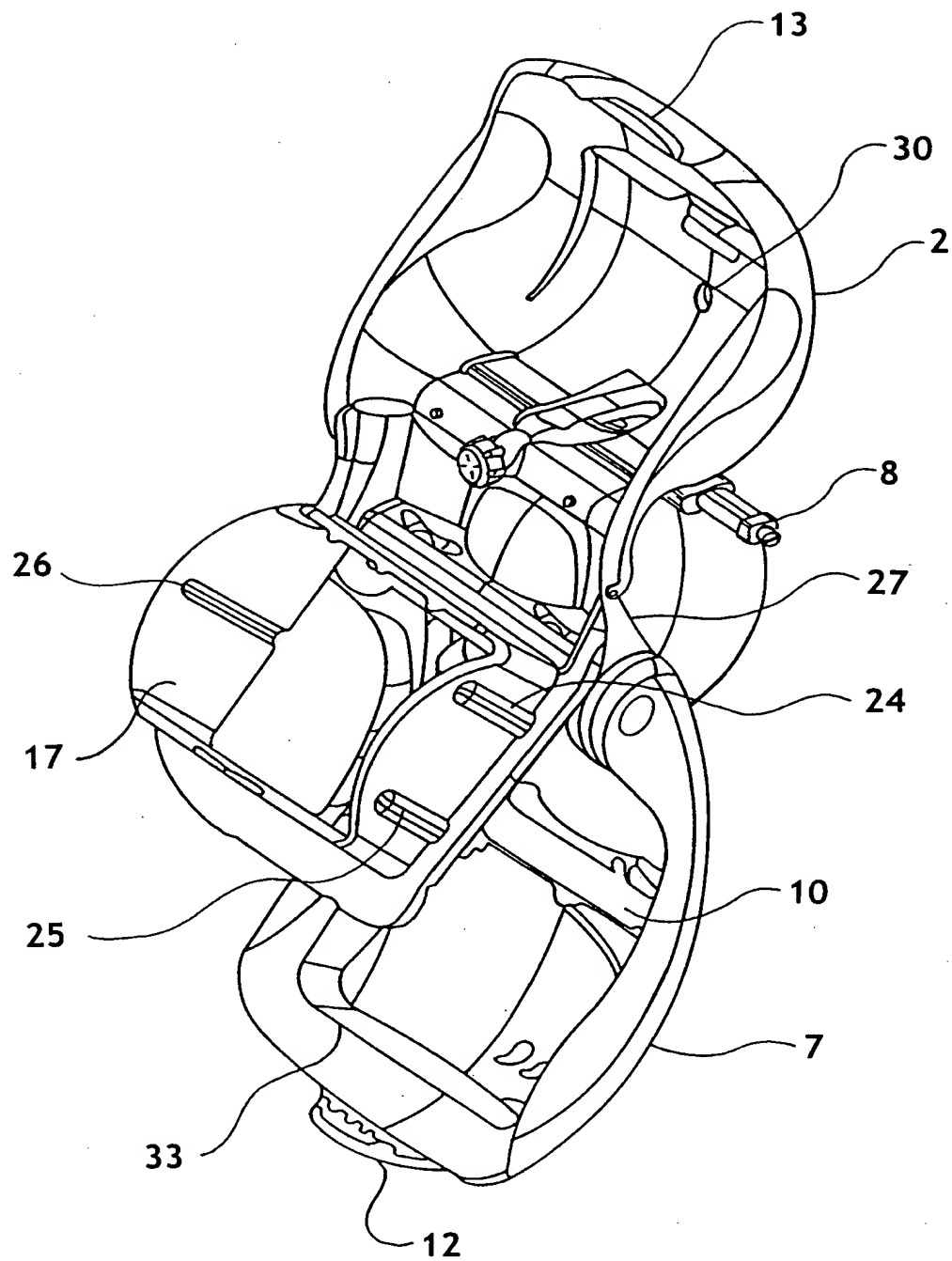


FIG. 3

FIG. 4

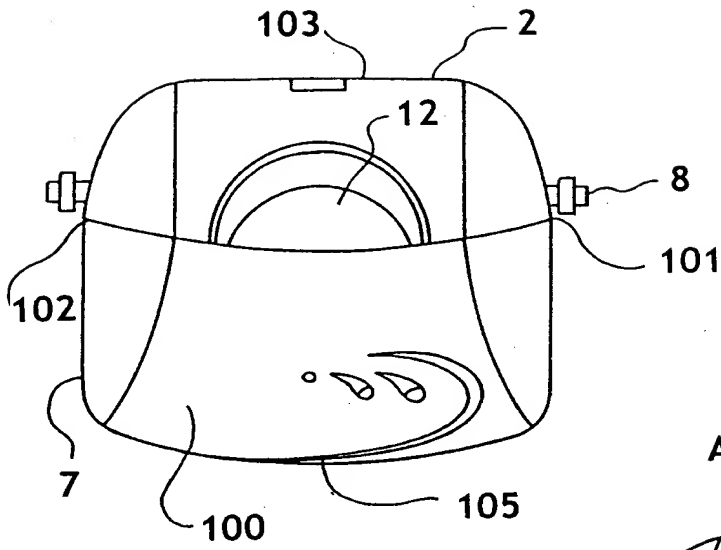


FIG. 4A

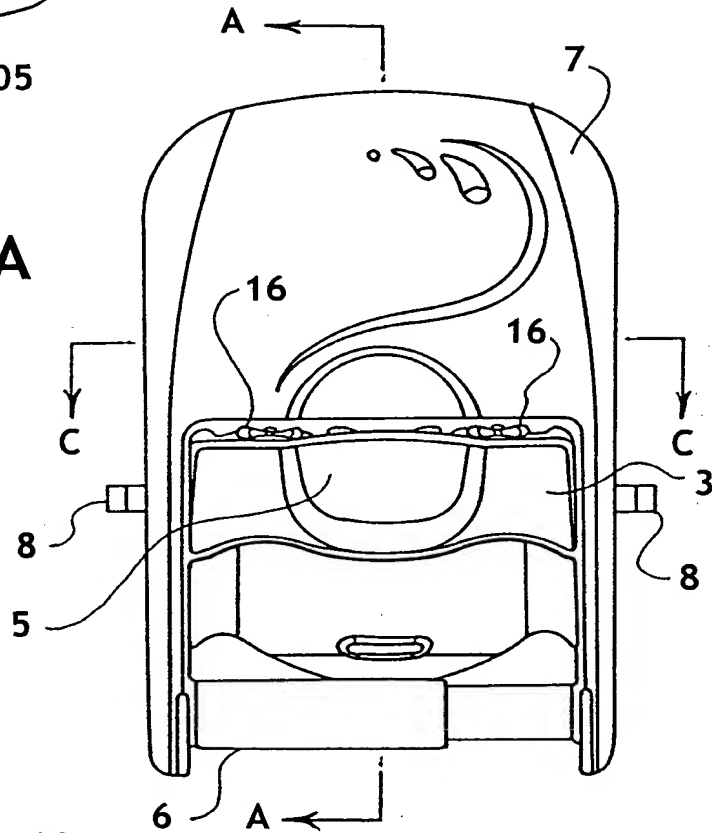
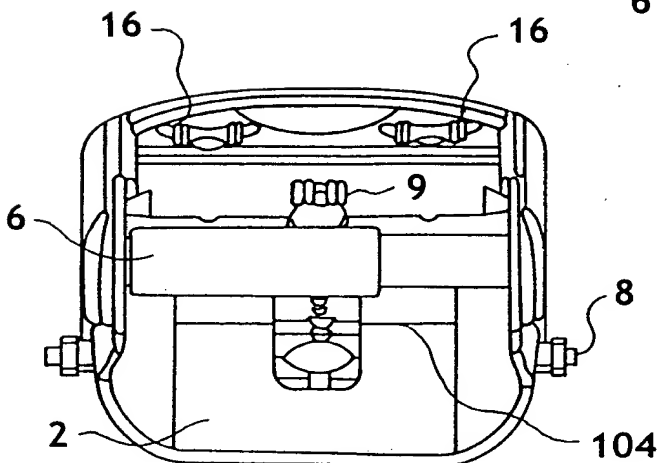


FIG. 4B



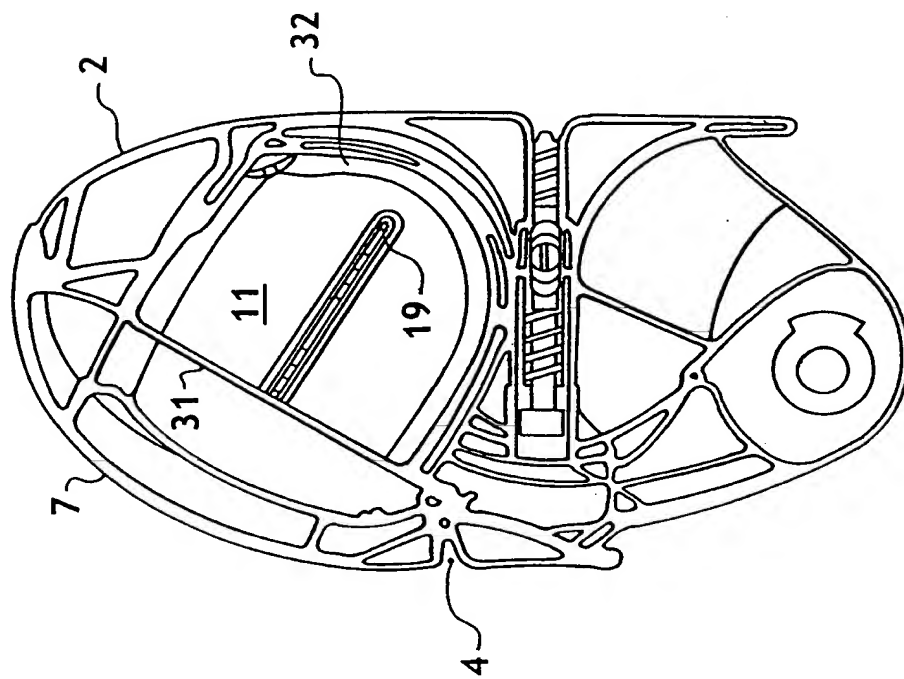


FIG. 5

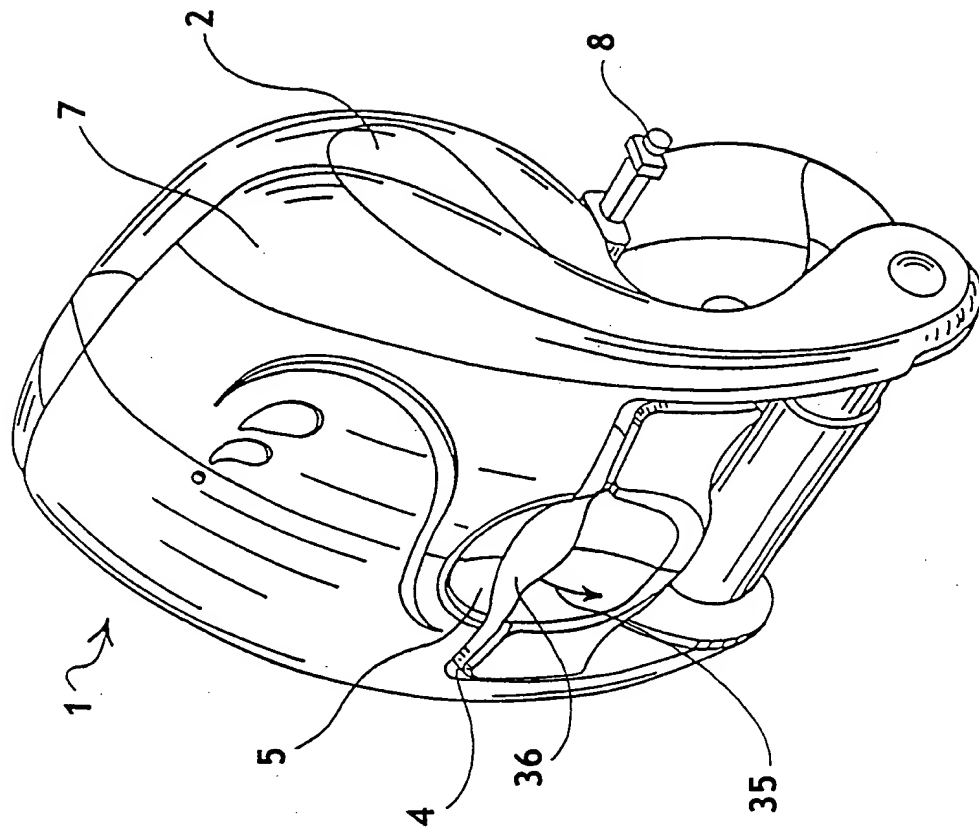


FIG. 6

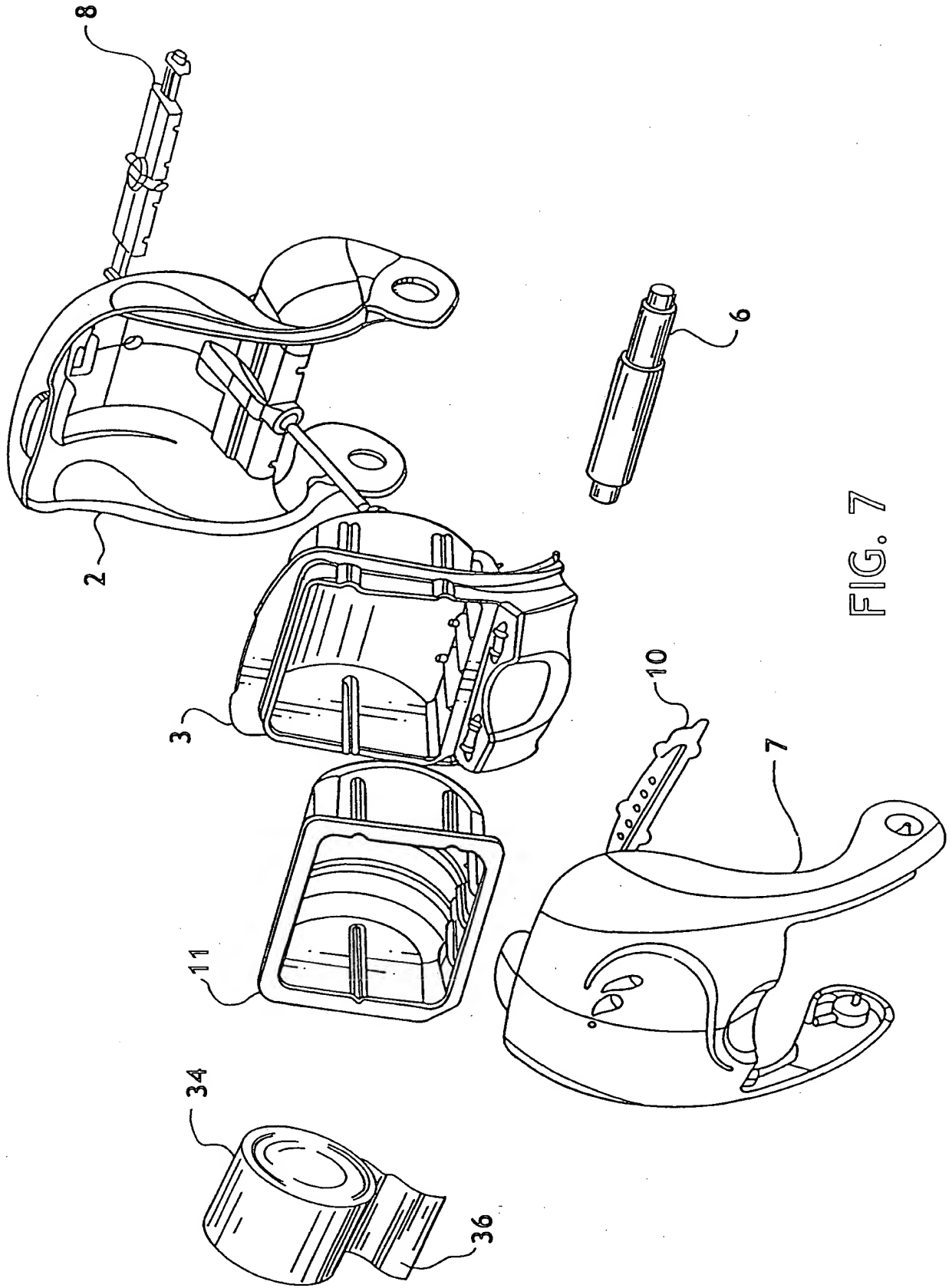


FIG. 7

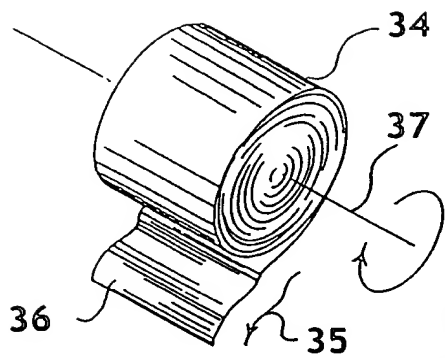


FIG. 8

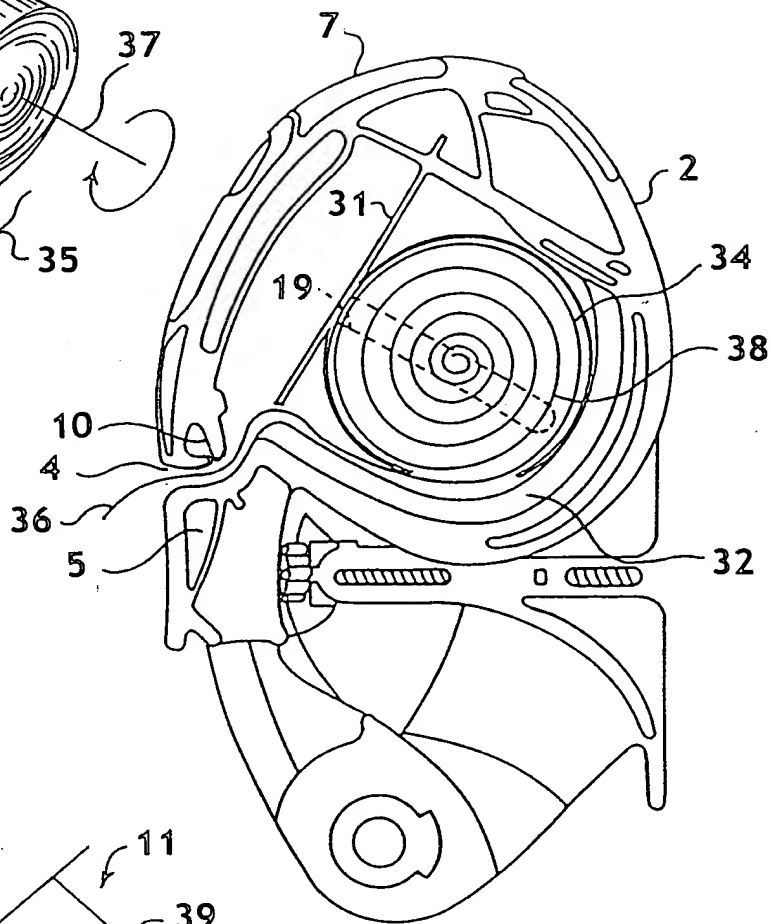


FIG. 9

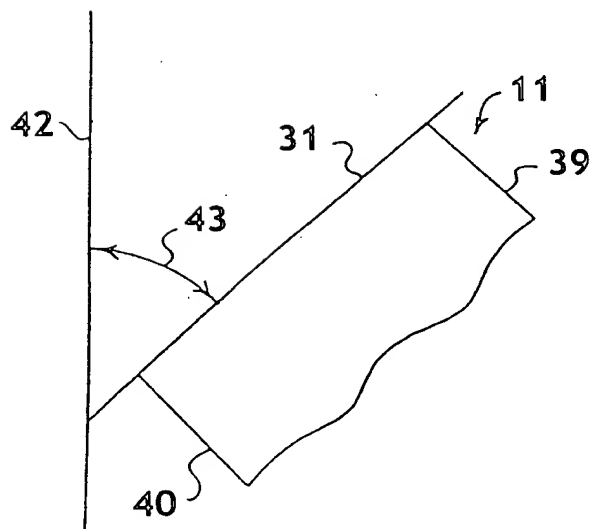


FIG. 10

09000559 070601

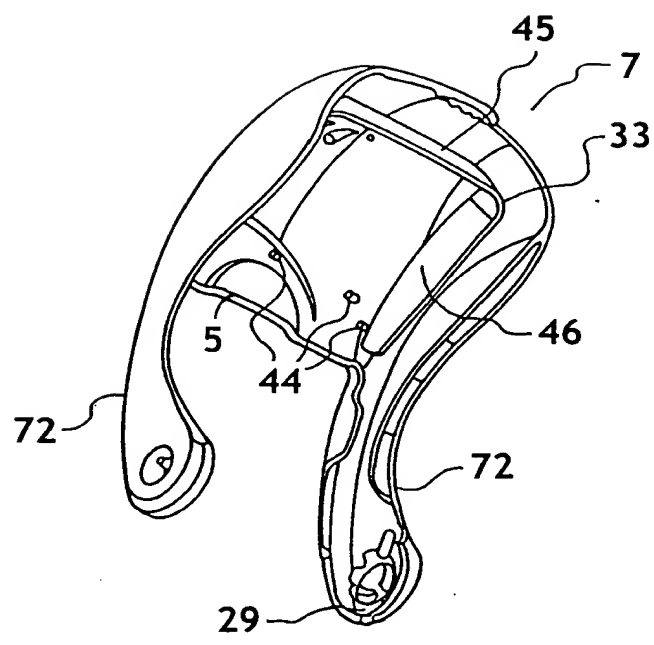


FIG. 11



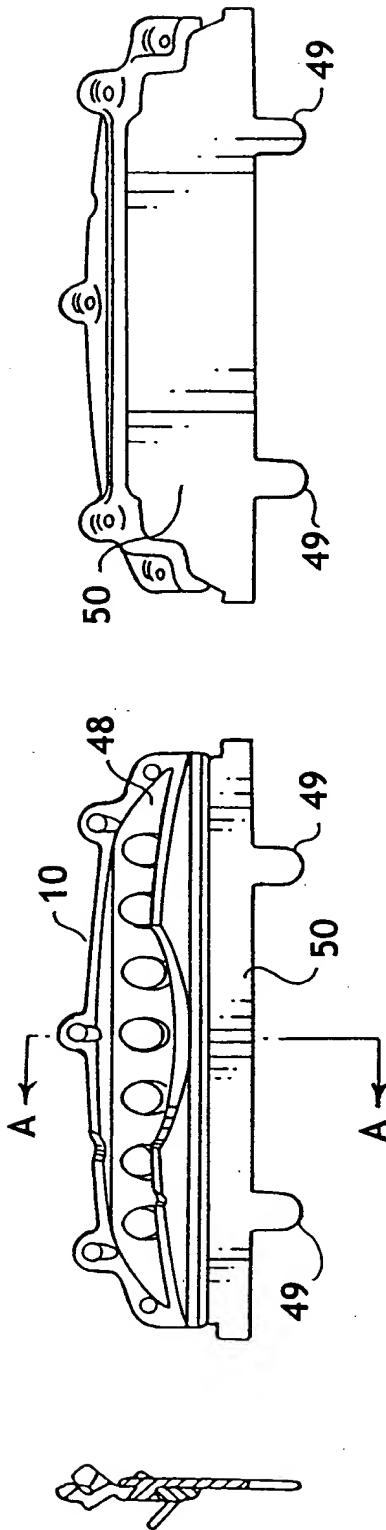


FIG. 12A

FIG. 12

FIG. 13

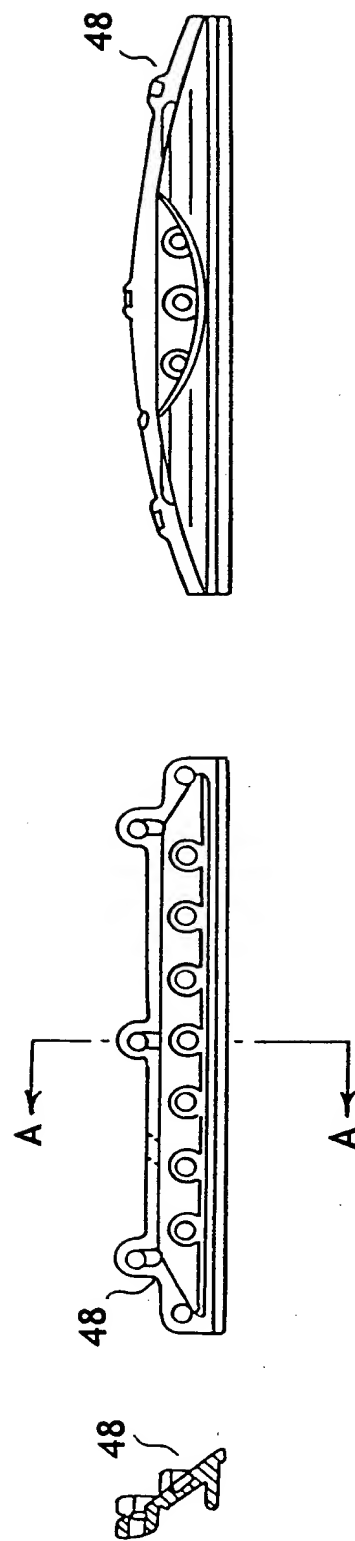


FIG. 15

FIG. 14

FIG. 16

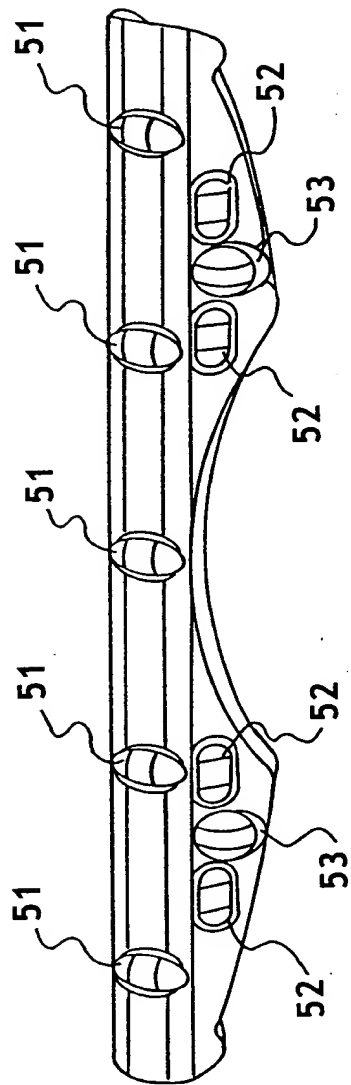


FIG. 17

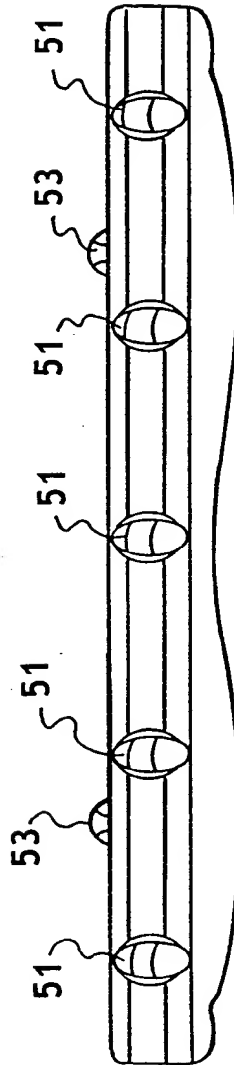


FIG. 18

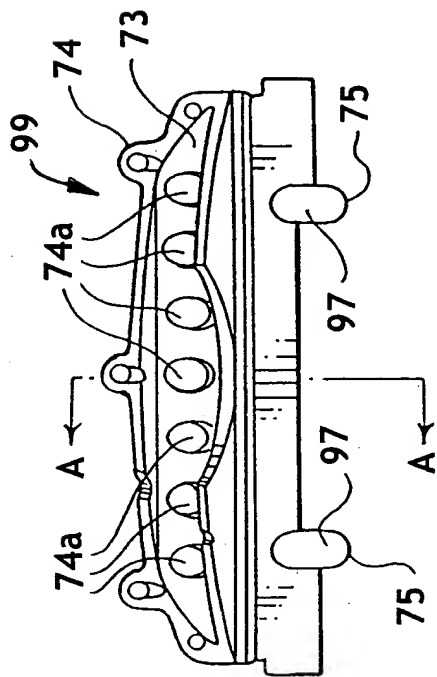


FIG. 19

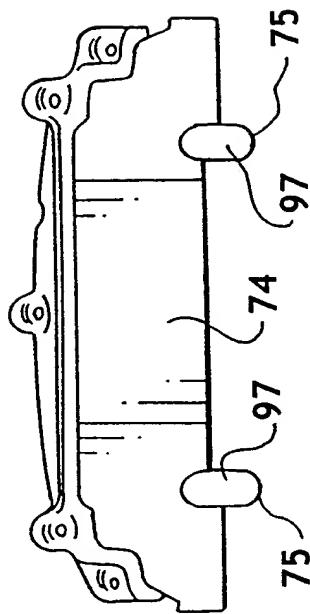


FIG. 20

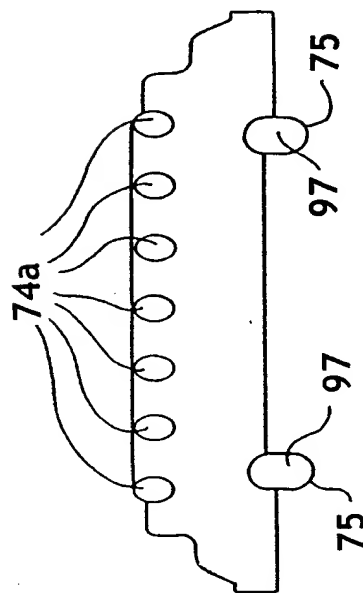


FIG. 21

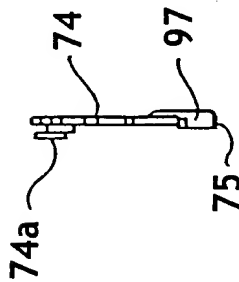


FIG. 22

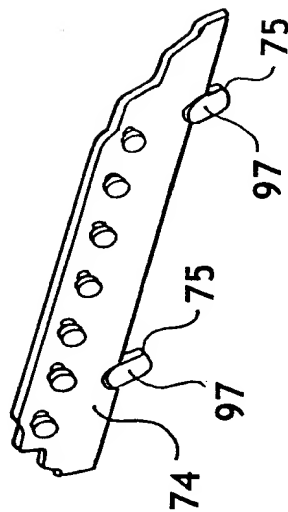


FIG. 23

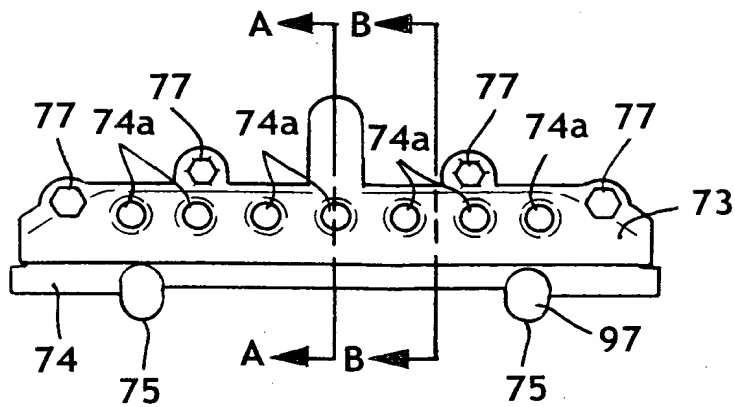


FIG. 24

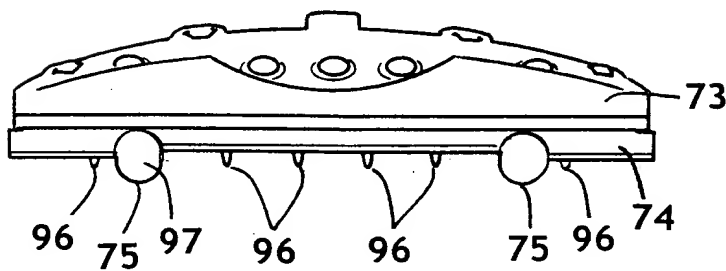


FIG. 27

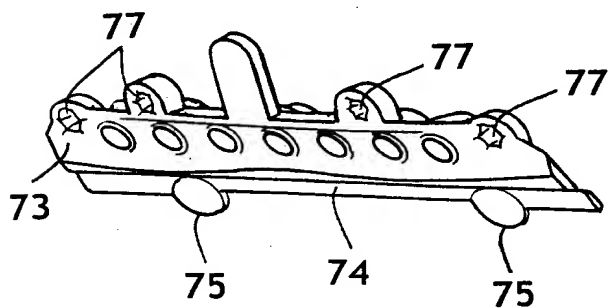


FIG. 28

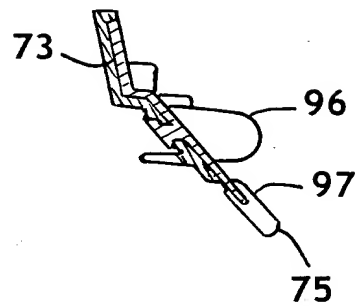


FIG. 25

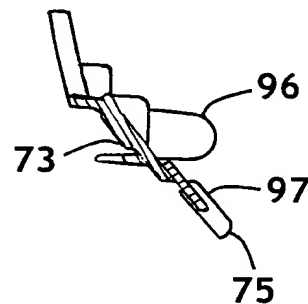


FIG. 26

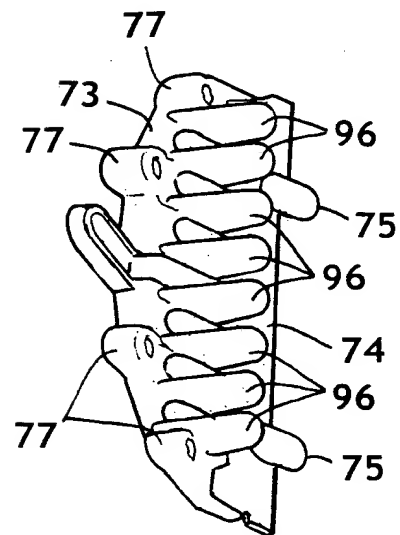


FIG. 29

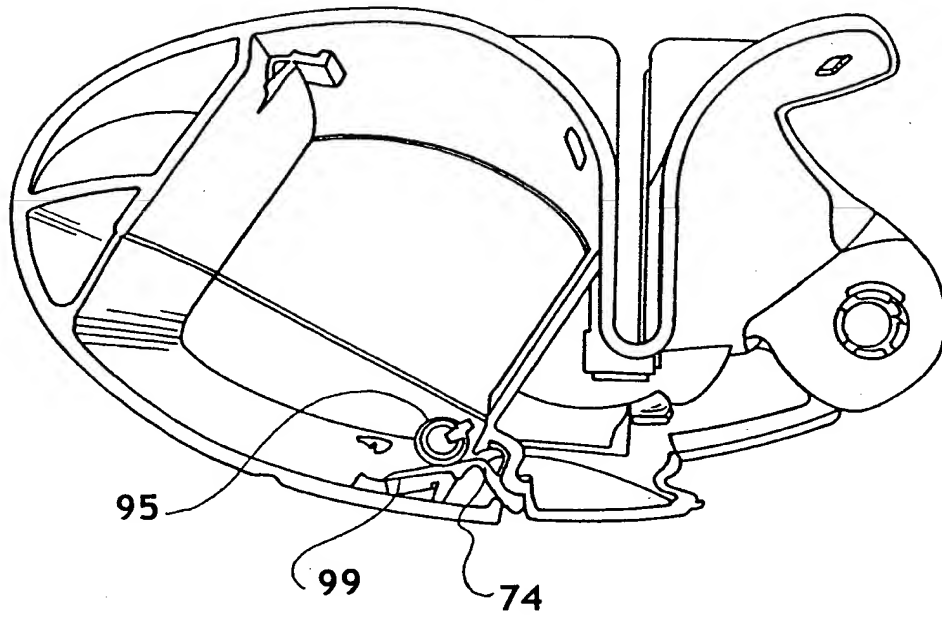


FIG. 30

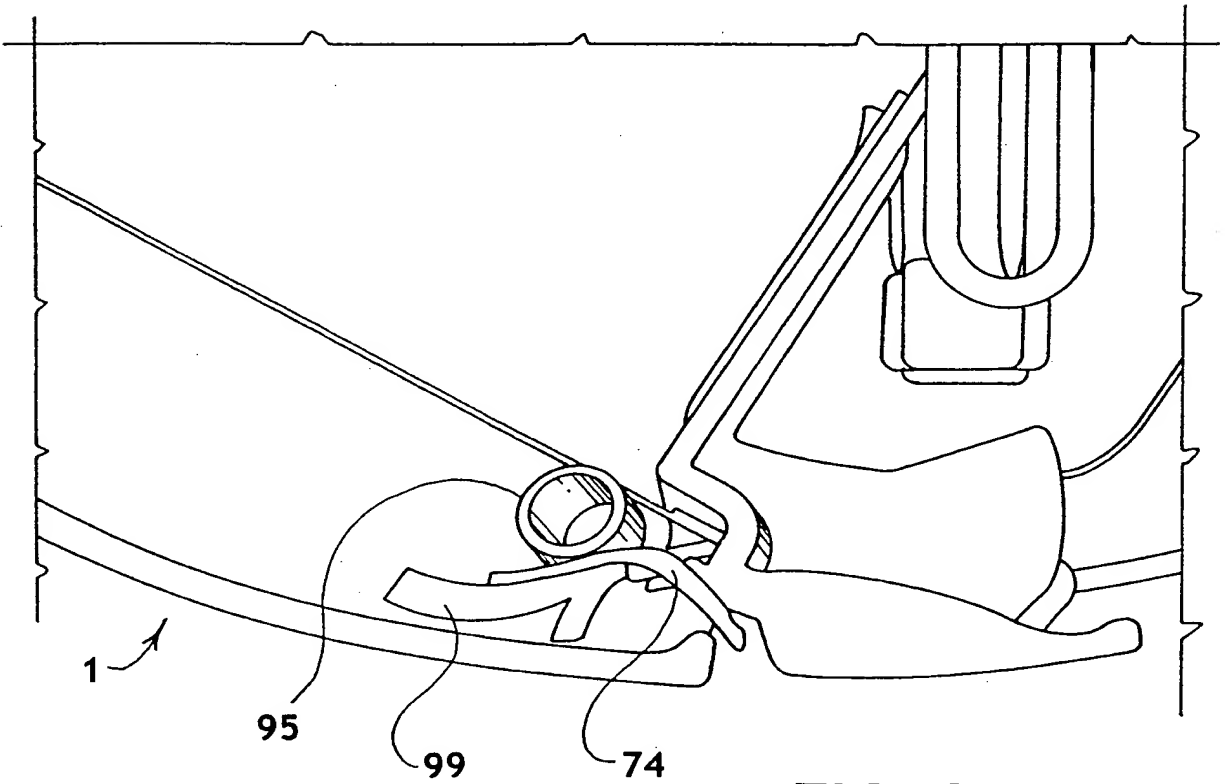


FIG. 31

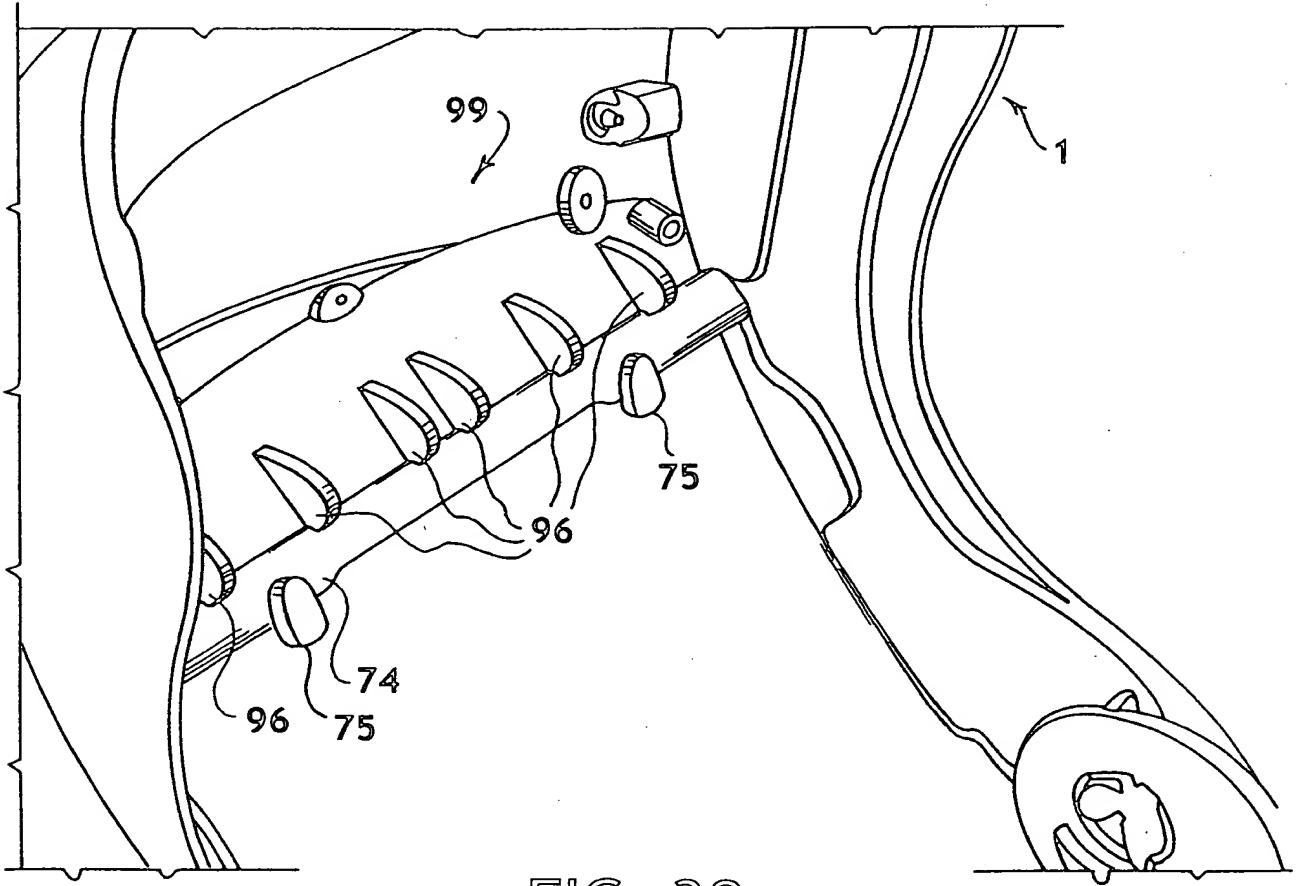


FIG. 32

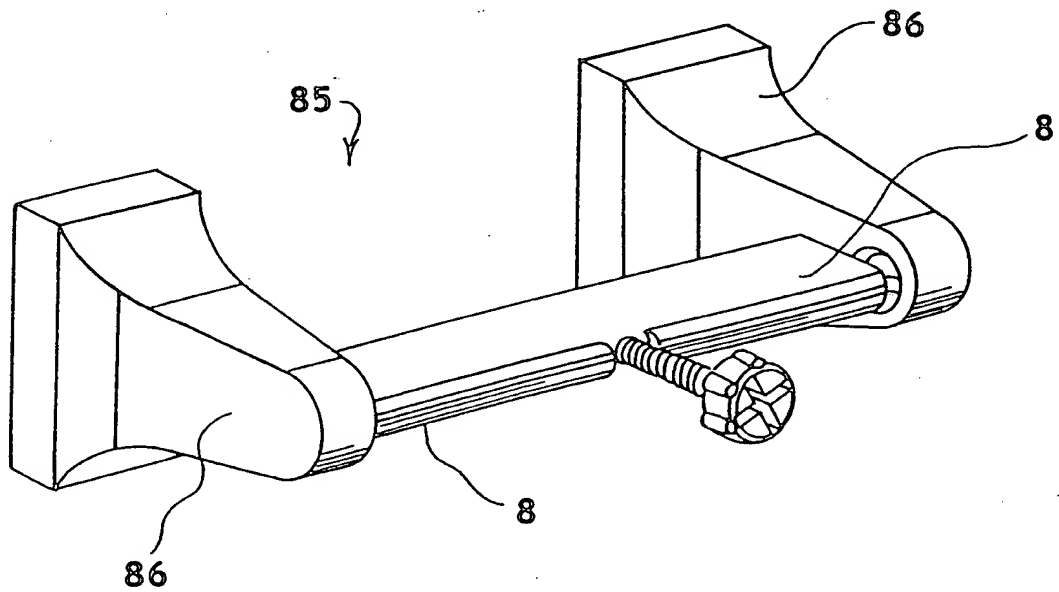


FIG. 33

FIG. 34

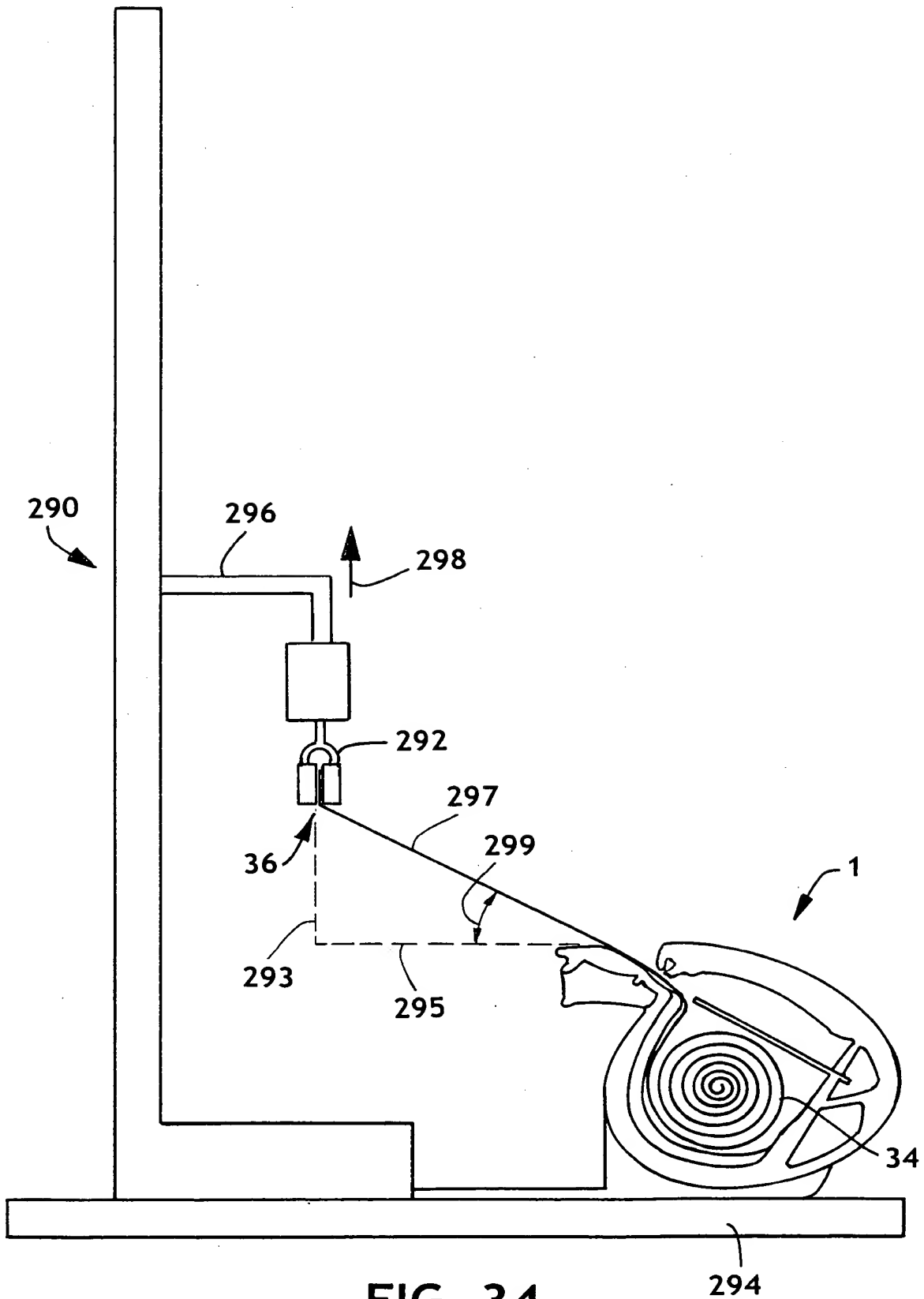


FIG. 35

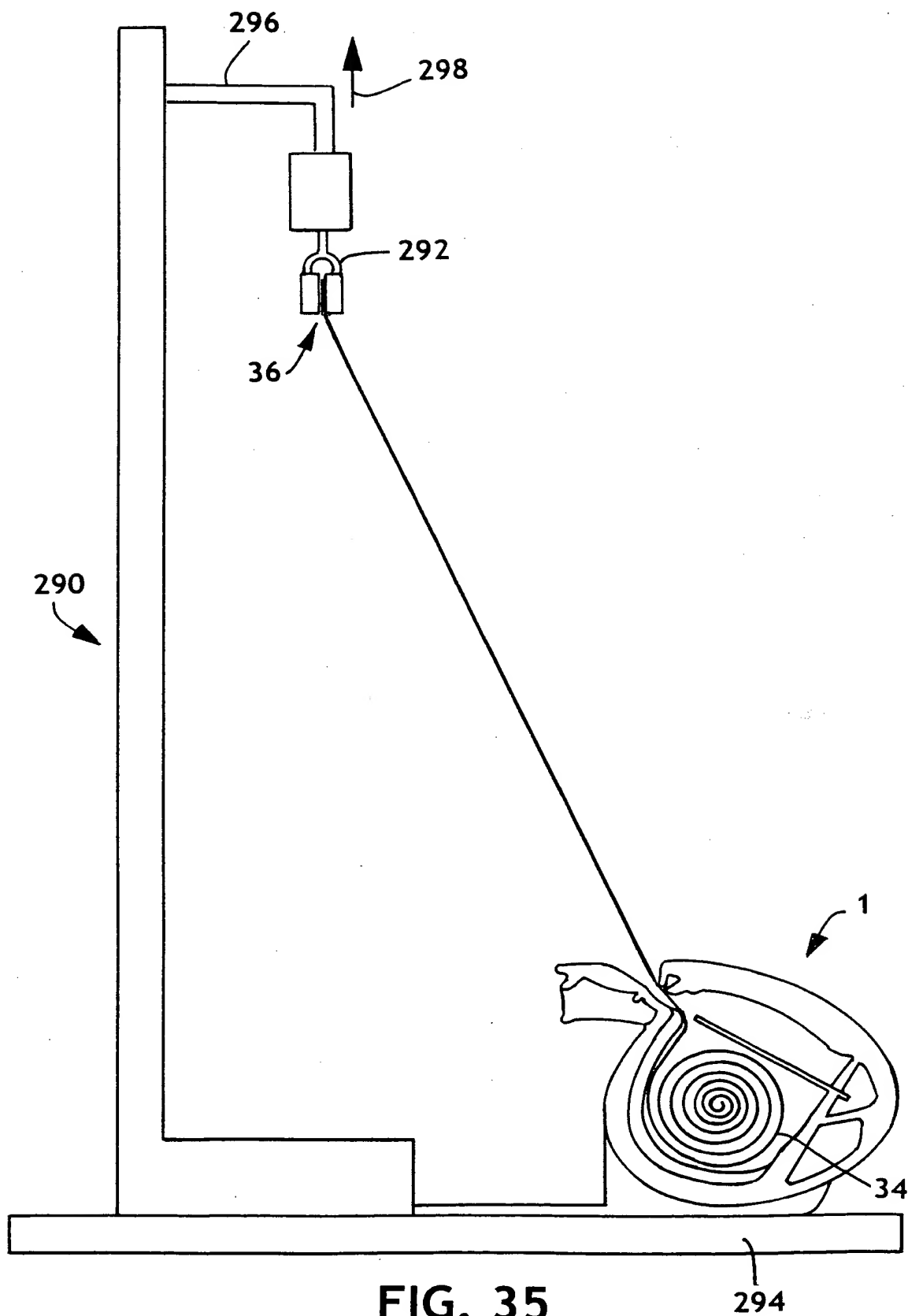


FIG. 35



FIG. 36

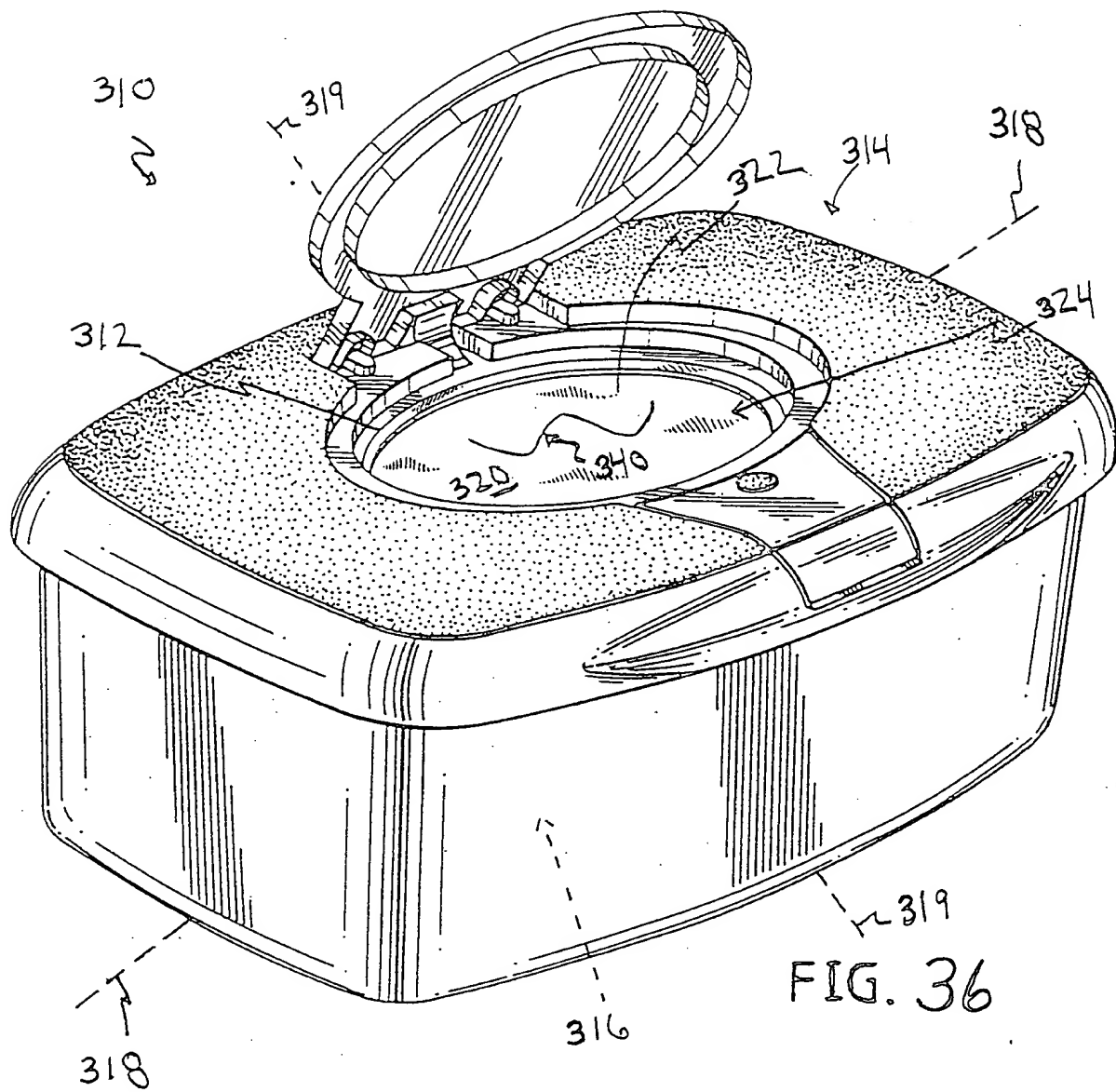


FIG. 36

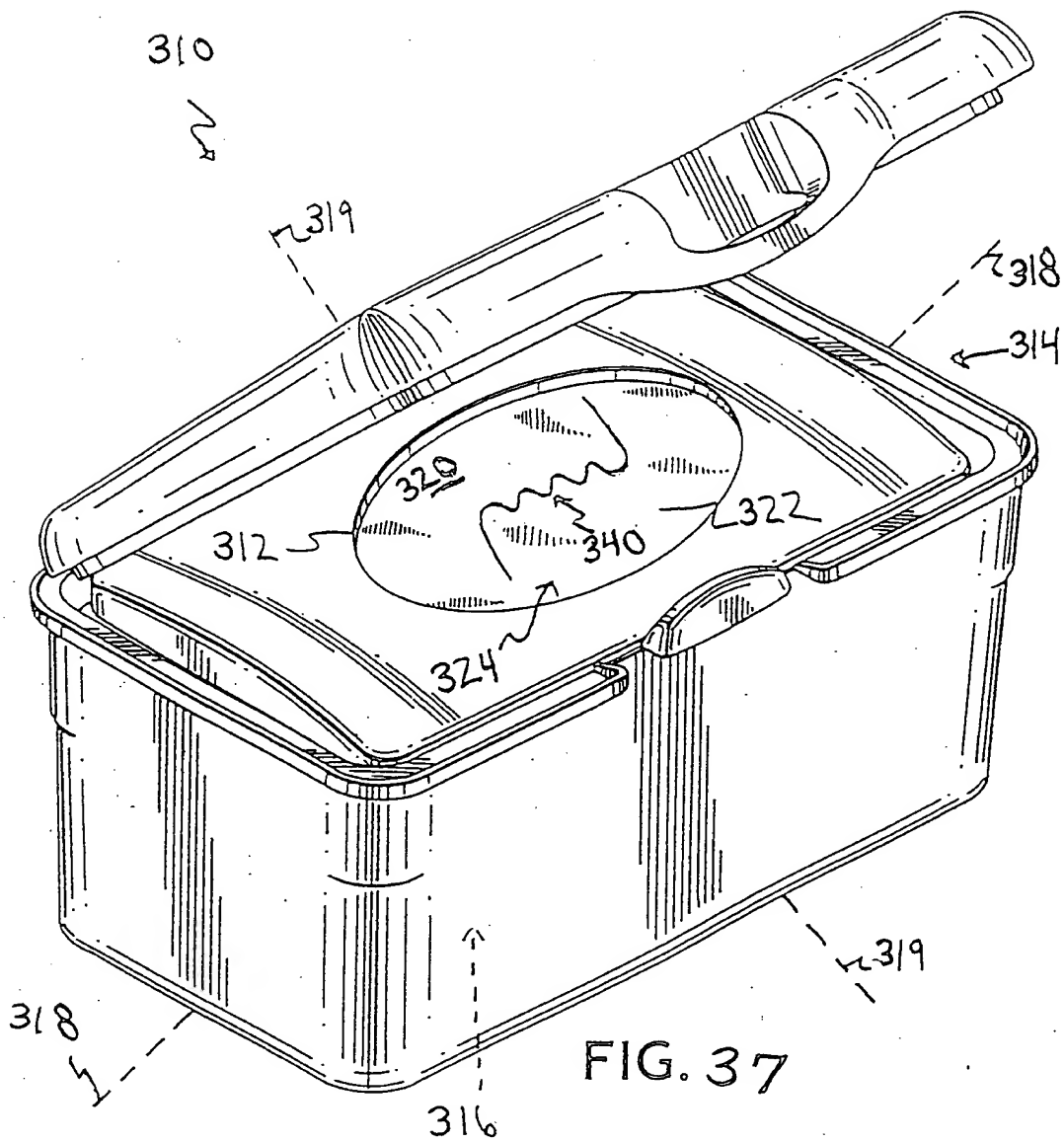




FIG. 39

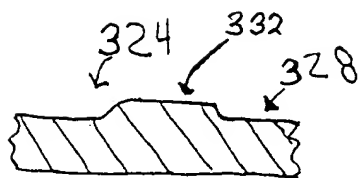


FIG. 39

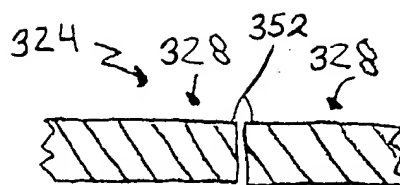


FIG. 40

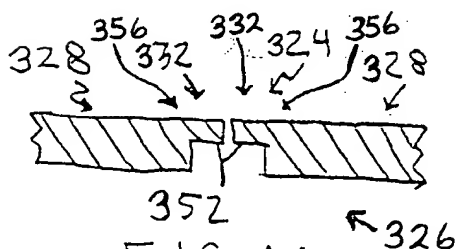


FIG. 41

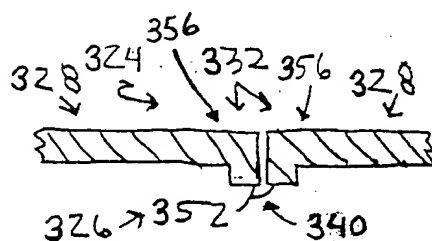


FIG. 42

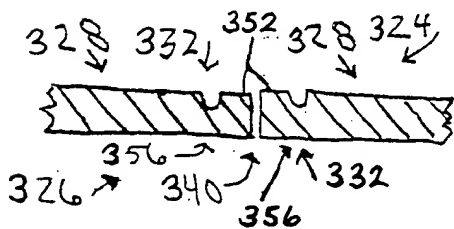


FIG. 43

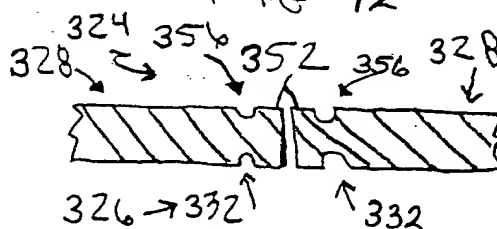


FIG. 44

A schematic diagram of a closed loop system, possibly representing a network or a circuit. The diagram features a large outer oval boundary. Inside this boundary, there are several smaller ovals and circles. A central horizontal dashed line runs through the middle of the diagram. Along this line, there are several nodes labeled with numbers: 334, 340, 348, 352, 332, 344, 334, and 342. Above the central line, there are additional labels: 328, 320, 344, and 342. Below the central line, there are labels: 322, 332, 342, 352, 324, and 328. Arrows indicate connections between various points, some pointing towards the center and others away from it. A dimension line at the top indicates a width of 354.

FIG. 45